



BES2600WM

Brief Datasheet

Smart Wi-Fi/Bluetooth Single Chip Audio Platform

CONTACT US:

Company: Bestechnic (Shanghai) Co., Ltd. (“BES”)

Address: 2F, Building B, Lane 2889 Jinke Road, Pudong, Shanghai (201203)

Phone: (86)21 6877 1788

For product inquiries and more information, please visit www.bestechnic.com.

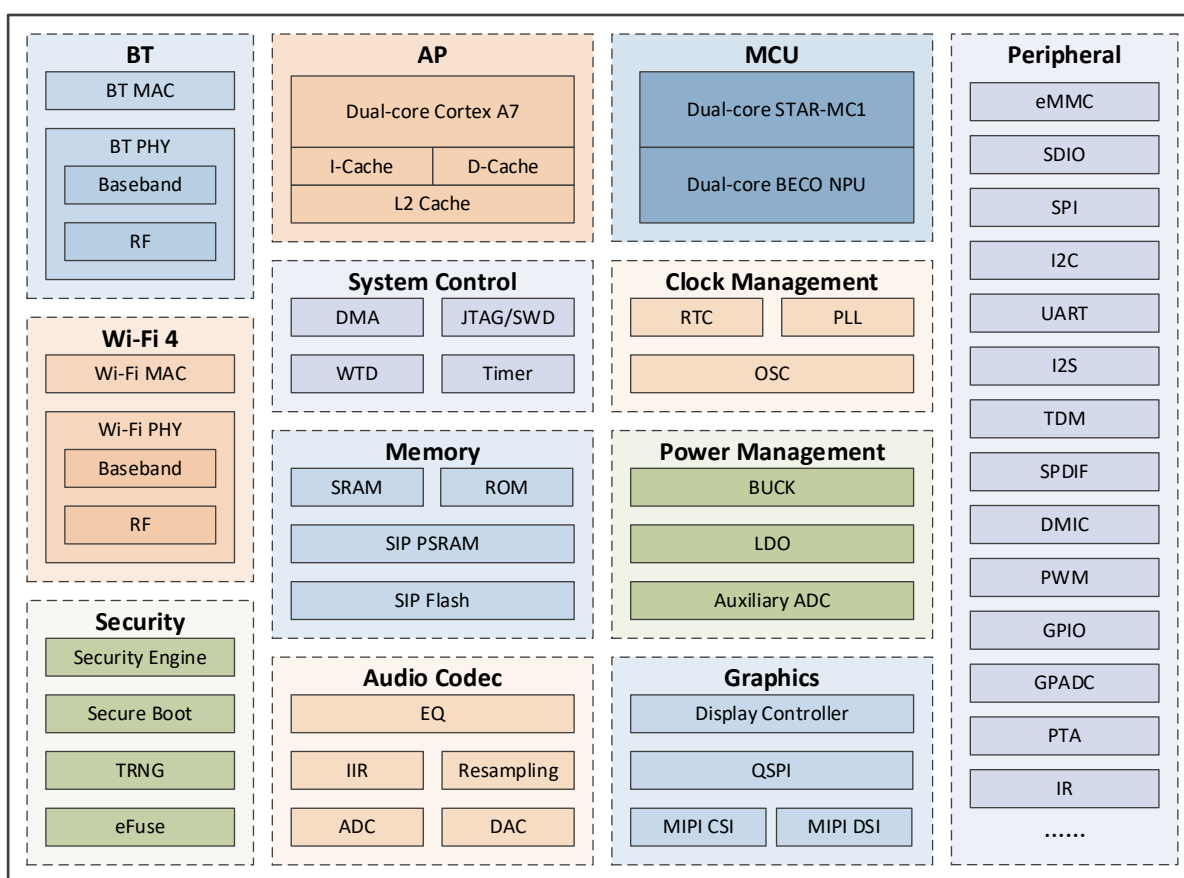
DISCLAIMER:

No part of this document may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, for any purpose, without the express written permission of BES. BES retains the right to make changes to this document at any time, without notice. BES makes no warranty of any kind, expressed or implied, with regard to any information contained in this document, including, but not limited to, the implied warranties of merchant ability or fitness for any particular purpose. Further, BES does not warrant the accuracy or completeness of the information, text, graphics, or other items contained within this document.

1 General Description

The BES2600WM is a highly integrated, high performance audio SoC with integrated Wi-Fi and Bluetooth. The platform incorporates a powerful AP subsystem comprising a dual-core Cortex A7 and a power-efficient MCU subsystem comprising a dual-core STAR-MC1 with a dual-core BECO NPU, a BES proprietary coprocessor for advance signal processing and NN workloads. The platform also includes a voice and audio codec subsystem that supports microphone arrays with up to three analog microphones or six digital microphones for far-field voice applications, as well as a graphics subsystem that includes a display controller and supports QSI, CSI and DSI interfaces.

Both the MCU and AP subsystems are capable of running RTOS and user applications. In addition, the MCU subsystem runs the Bluetooth upper protocol stack, while the AP subsystem runs voice and audio processing and AI tasks. The Wi-Fi and Bluetooth subsystems integrate separate RF circuitry for optimized coexistence performance. The highly integrated design minimizes external components and reduces BOM costs.



System Block Diagram

1.1 Applications

- Smart wireless speakers with far-field voice
- Wireless docking stations and soundbars
- Smart wireless displays
- Other wireless IoT devices

1.2 Features & Specifications*

AP Subsystem	Dual-core Cortex-A7
MCU Subsystem	Dual-core STAR-MC1
Memory and Storage	Shared 2 MB SRAM
	Flash and PSRAM in package
	boot ROM
Wi-Fi/Bluetooth Subsystem	Dual-Band 2.4G & 5G Wi-Fi IEEE 802.11 a/b/g/n
	Dual-mode BT 5.4 with LE audio
Audio & Voice Features	2x DACs
	3x ADCs
Graphics & Multimedia	2.5D Vector GPU
	MIPI DSI
Peripheral Interfaces	SDIO/PTA/eMMC/SPI/I2C/UART/I2S/TDM/SPDIF/DMIC/PWM/GPIO/GPADC/IR.....
Package	169-pin BGA

* The content in the table is subject to change without notice.